Test Case1.)

Diagram

Description automatically generated

Already solved:

Text

Description automatically generatedText

Description automatically generated

Test Case 2.)

Diagram

Description automatically generated

Here for the callee it will be the same as above but for Ap(that is entry IPA will differ)

Diagram

Description automatically generated

Test Case 3.)

Diagram

Description automatically generated

Compared to Example 1 IPA liveout of Dp should have (a,b,f,g), it was empty in 1

Diagram, schematic

Description automatically generated

Test case4.)

Diagram

Description automatically generated

New connection between Aq and Eq

Diagram

Description automatically generated

Test case 5.)

Diagram

Description automatically generated

In this case there is a connection from Dp to Ap

First pass liveout dp:mn

Second pass-